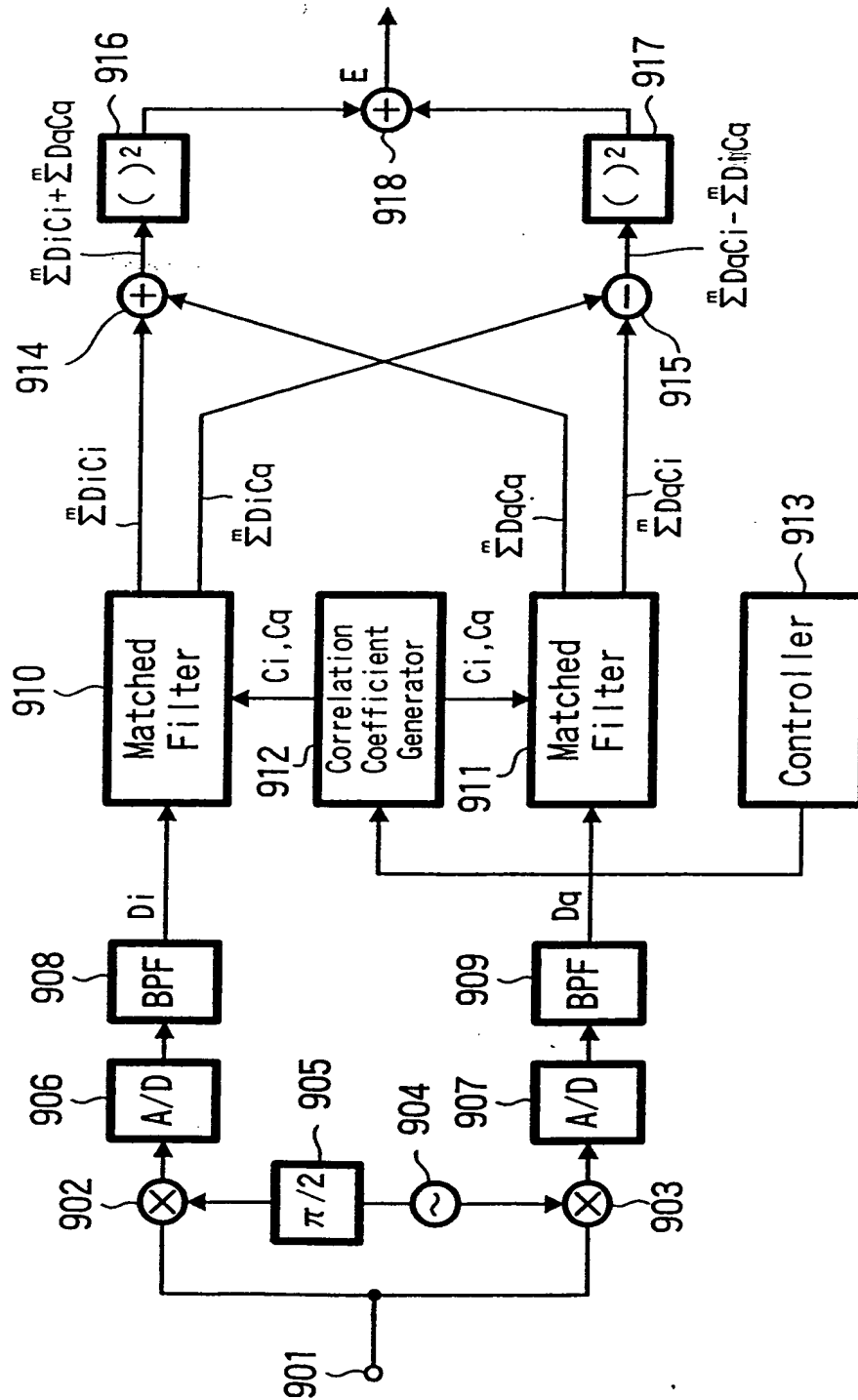




FIG. 2

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(When The Number of Shift Registers of The Matched Filter is  $m$ )

Number of Correlation Added Chips= $m$   
Searchable Range Number= $m \times n$  Chips

FIG. 3A

State of The Shift Registers In The Matched Filter



(M-Bit) State of The Correlation Coefficient Inputted to The Matched Filter (=The Output of The Correlation Coefficient Generator)

FIG. 3B

Matched Filter Output



FIG. 3C

Memory Address

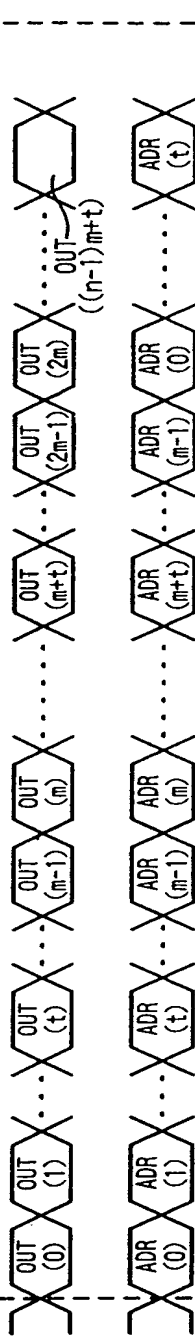
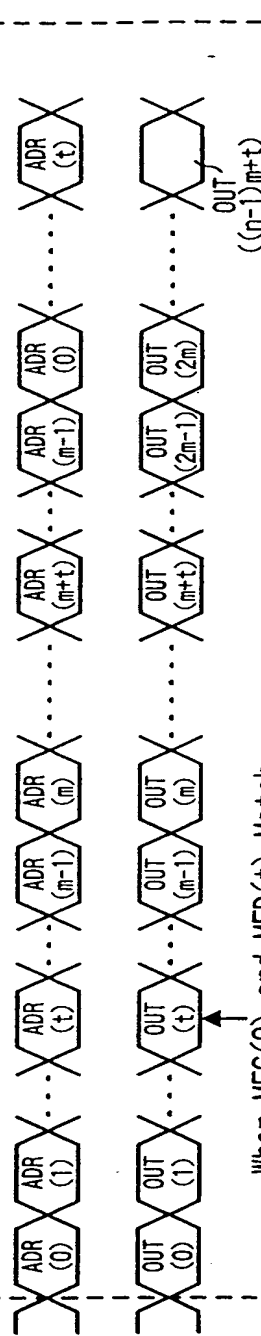
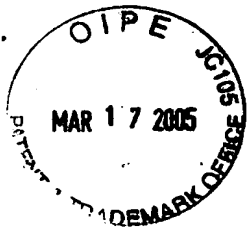


FIG. 3D

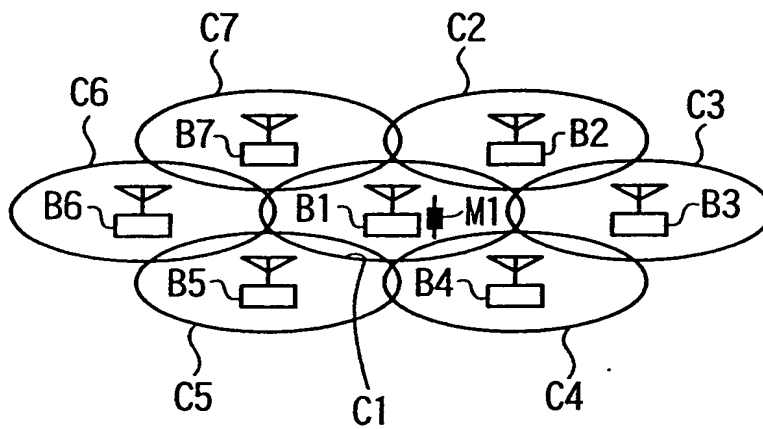
Value Written to The Memory



When MFC(0) and MFD(t) Match,  
This Value is The Maximum Value.



**FIG. 1**



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